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21171 7590 08/12/2003

STAAS & HALSEY LLP
SUITE 700
1201 NEW YORK AVENUE, N.W.
WASHINGTON, DC 20005



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(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/023,769 12/21/2001 Hiroko Murakami 1076.1023D2 4629

TITLE OF INVENTION: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE INCLUDING A CIRCUIT FOR CANCELING AN OFFSET VOLTAGE OF AN A/D CONVERTER

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
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nonprovisional NO \$1300 \$300 \$1600 11/12/2003

EXAMINER	ART UNIT	CLASS-SUBCLASS
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HOLDER, REGINA NEAL 2651 360-039000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

1 Staas & Halsey LLP
2 _____
3 _____

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Fujitsu Limited

Kawasaki, Japan

Please check the appropriate assignee category or categories (will not be printed on the patent) ☐ individual ☒ corporation or other private group entity ☐ government

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Derrick L. Fields 11-4-03
(Authorized Signature) (Date)

Derrick L. Fields, Reg. No. 50,133

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11/06/2003 JADD02 00000081 10023769

01 FC:1501

1330.00 EP

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300.00 EP

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